



**22<sup>nd</sup> International Symposium on  
VLSI Design and Test (VDAT 2018)**  
June 28<sup>th</sup> - 30<sup>th</sup>, 2018  
Thiagarajar college of Engineering  
Madurai, TamilNadu, India  
Website: <http://vdat2018.tce.edu>

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VLSI Society of India  
<http://vlsi-india.org/vlsi/>

**REGISTRATION FORM**

<b>Name of the participant:</b> (Please use block letters.)				
<b>Category:</b> (Tick appropriate)	Fellow	<input type="checkbox"/>	VDAT 2018 Fellowship No:	
	Faculty / Student: VSI/ IEEE member	<input type="checkbox"/>	VSI/ IEEE Membership No:	
	Indian Industry: VSI/ IEEE member	<input type="checkbox"/>	VSI/ IEEE Membership No:	
	Author	<input type="checkbox"/>	Paper No/s:	
	Foreign Participant	<input type="checkbox"/>		
<b>Address for Correspondence:</b>	<b>Designation:</b> Student    Faculty    Industry    Other			
	<b>College/ Company Name:</b>			
	<b>Permanent address:</b>			
<b>E-mail:</b> (Compulsory):		Mobile:		
<b>Choice of tutorial:</b>	T1 <input type="checkbox"/> / T2 <input type="checkbox"/>	T3 <input type="checkbox"/> / T4 <input type="checkbox"/>		
<b>T1:</b> Taking Reuse to Next Level: Exploiting Transaction Level Modeling (TLM) for Universal Verification Methodology (UVM) <b>T2:</b> Recent trends in Modeling and simulation of defects in analog circuits and their applications <b>T3:</b> Tunnel Field Effect Transistors and Re-configurable Device: A New Paradigm in Emerging Device Technology <b>T4:</b> IoT Security: the darker side of the cloud				
<b>Details of DD enclosed</b> (** Refer below table for Registration Amount)	Tutorial (2 half day)		DD Number:	
	Symposium (Participation only)		Dated:	
	Tutorial+Symposium (Participation only)		Bank:	
	Paper			
	Short Paper			
	Poster			
	PhD Forum			
	Design contest			
	<b>Total Amount:</b>			
<b>** Registration amount for Tutorial and Symposium:</b>		<b>Registration amount for Paper Presentation, Short Papers, Poster Presentation , Ph.D Forum, Design Contest</b>		
<b>Event</b>	<i>Amount in Rs</i>	<b>Event</b>	<i>Before June 15, 2008</i>	<i>After June 15, 2008</i>
Tutorial (Student)(2 half day)	Rs 500	Paper presentation	Rs.5000	Rs.7000
Tutorial (Faculty) (2 half day)	Rs 1000	Short papers	Rs.5000	Rs.7000
Participation (Tutorial + Symposium)	Rs 3000	Poster presentation	Rs.5000	Rs.7000
Participation(Symposium only)	Rs 2500	PhD Forum	Rs.3000	Rs.3500
		Design contest	Rs. 1000	Rs. 1000
Mail the completed registration form and DD to the following address: <b>Dr.V.R.Venkatasubramani Finance Chair, TCE Madurai Phone: 9894243364 e-mail : venthiru@tce.edu</b>		<ul style="list-style-type: none"><li>• The DD to be made out to "<b>THE PRINCIPAL, THIAGARAJAR COLLEGE OF ENGINEERING</b>" payable at <b>ICICI, TCE Branch, Madurai, India.</b></li></ul> <b>Note:</b> Symposium registration fee includes registration material, lunch and refreshments on all the days. If registering separately for Symposium and Tutorial, please submit separate forms for each.		