VDAT-2018

22nd International Symposium on VLSI Design and Test

Department of Electronics and Communication Engineering

Thiagarajar College of Engineering, Madurai, India

28th June – 30th June 2018

Call for Papers

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If you have any query please contact:
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Registration Details

For Paper Submission, registration and other Details, please visit
www.tce.edu/vdat2018
http://vdat2018.tce.edu/
The papers are to be submitted through the easy chair link :
https://easychair.org/conferences/?conf=vdat2018

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About VDAT

VDAT began as a small workshop in the year 1998. In 2005, it acquired the status of a Symposium. The purpose of the Symposium is to promote the advancement of all aspects of VLSI technology, primarily in India. The 22rd International Symposium on VLSI Design and Test (VDAT-2018) is to be held in Thiagarajar College of Engineering, Madurai, India. The aim of this symposium is to bring together academics, researchers and industrial practitioners to exchange their ideas in the field of VLSI design and testing.

About TCE

Founded in 1957 by Philanthropist and Industrialist Late Shri.Karumuttu Thiagarajan Chettiar, Thiagarajar College of Engineeing (TCE) is an ISO 9001:2008 certified institution affiliated to Anna University and situated in Madurai, the Temple City. The college is funded by central & State Governments and Management. The courses offered in TCE are approved by the All India Council for Technical Education, New Dehi. TCE was granted Autonomy in the year 1987 and has been accredited by the National Board of Accreditation (NBA). TCE offers 8 undergraduate programmes, 13 postgraduate programmes and doctoral programmes in Engineering, Science and Architecture.

About ECE Department

The Electronics and Communication Engineering Department of this college has been established in 1978. The Department offers an UG programme in Electronics and Communication Engineering and PG Programmes on Communication Systems and Wireless Technologies. This DST FIST supported Department has completed 40 research projects with research organization like DRDL, RCI, DEAL and consultancy works for companies like Motorola, Honeywell, Texas Instruments, National Instruments, Amphenol Antel. The Department has well established laboratories like Altera VLSI Lab, Agilent Communication Lab, National Instruments Electronics System Laboratory, Freescale Embedded Lab, Signal Processing Lab, Image Processing and RF Lab. Thiagarajar Telekom Solutions Ltd is a research-driven wireless design company, incubated out of 25 years of rich R&D experience in the field of RF/Microwaves in the TIFAC-CORE at Thiagarajar College of Engineering, Madurai.

Important Dates / Deadline

Regular Paper

Last date for Paper Submission   : 1st April 2018
Notification of Acceptance   : 16th May 2018
Camera Ready Version   : 31st May 2018

Tutorial

Tutorial Proposal Submission   : 15th April 2018
Tutorial Announcement   : 1st May 2018

Ph.D. Forum

Submission   : 1st April 2018
Notification of Acceptance   : 19th April 2018
Camera Ready Version   : 26th April 2018

Student Design Contest

Submission   : 15th April 2018
Notification of Acceptance   : 1st May 2018

The paper accepted for oral presentation will be recommended to Communications in Computer and Information Science (CCIS) proceedings publication service to include it on the Springer website.

Call For Papers

Researchers, academicians and professionals are invited to submit papers in the following topics (but not limited to)

VLSI Device Modelling

- Device modeling and Simulation.
- Advanced CMOS Devices
- Emerging memory devices
- Reliability Physics, Characterization and test
- Multi-gate/ FDSOI device-circuit interaction.

Analog & Mixed Design

- CMOS Analog, Mixed-Signal and digital circuits.
- Circuit timing and Power models.
- Low Power and near/sub threshold circuit design
- Analog, Mixed-Signal and digital system design.
- Reconfigurable logic/computation.
- Design For Manufacturability.

CAD for VLSI

- Logic and behavioural synthesis.
- Placement, routing and floorplanning.
- CAD tools.
- Design Automation.

Testing and Verification

- Design Verification, Test, Reliability and Fault Tolerance
- Formal Verification
- Design for testability
- Fault Modeling
- Post-silicon Validation
- Testing memories and regular logic arrays.
- Design for manufacturability and yield analysis.

Emerging Technologies

- MEMS / NEMS
- Organic Electronic
- Spintronics
- 2D Material for electronic devices.
- Device physics design and circuits using non- silicon materials

There will be a double blind review for each paper. Authors' name should not be included in the submitted paper. A Paper with authors' names will not be considered for review. All the papers will be sent for review after checking the plagiarism. The paper must include an abstract of about 250 words and maximum of five keywords related to the topic of the paper. The acceptance of the paper is based on the following factors: The purpose of the work; the manner and degree to which it advances the art; Specific new results that have been obtained and their significance.