22nd International Symposium on VLSI Design and Test (VDAT 2018)
28th June- 30th June 2018

Thiagarajar College of Engineering

Madurai, TamilNadu, India

Website: http://vdat2018.tce.edu

FELLOWSHIP APPLICATION FORM

Name of the Fellowship Applicant: (Please use block letters.)

Address for Correspondence:	Designation: Student ☐ / Faculty ☐
	(You should be a student or a faculty member at the time of Symposium to avail fellowship – Otherwise, the fellowship stands cancelled.)
	College/ Institute Name:
	Institution and Departmental Address:

E-mail: (Compulsory):	Telephone:

Is your parent institution ready to partially support you?	Yes / No
Have you submitted paper for VDAT **2018**?	Yes / No
If selected, mention paper ID	
Have you received a Fellowship to attend VDAT before?	Yes / No

Applicant's Signature

- Email the completed application form to the following address:
  Dr.V.R.Venkatasubramani
  Finance Chair, TCE Madurai
  e-mail : **venthiru@tce.edu**
- Send the form to reach the Finance Chair through e-mail by **June 15, 2018**.
- **Subject of the email should be: "Application for Fellowship VDAT 2018".**

- If you are granted fellowship, you are expected to register before **June 19, 2018** and attend the event on all days.
- Queries regarding registration must be sent only to:**venthiru@tce.edu**

Note:	A fellowship should not be viewed as an honor or recognition. Fellowship entitles registration fee waiver. No other support will be available. Accommodation will be arranged upon request in College hostel at nominal cost.